



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant:** Kazuo Nishiyama      **Atty. Docket No.** 075834.00074  
**Serial No.:** 09/ 843,630      **Group Art Unit:** 2812  
**Filed:** April 27, 2001      **Examiner:** James M. Mitchell  
**Invention:** CHIP-LIKE ELECTRONIC COMPONENTS, A METHOD OF  
MANUFACTURING THE SAME, A PSEUDO WAFER THEREFOR AND  
A METHOD OF MANUFACTURING THEREOF

**DRAWING AMENDMENT**

Commissioner of Patents  
Washington, D.C. 20231

ATTN: Official Draftsman

S I R:

Applicant has attached proposed changes to Figures 9-12. The proposed changes are indicated in red ink. Applicant respectfully requests that the changes be accepted and entered in this application.

Respectfully submitted,

Date: December 30, 2002

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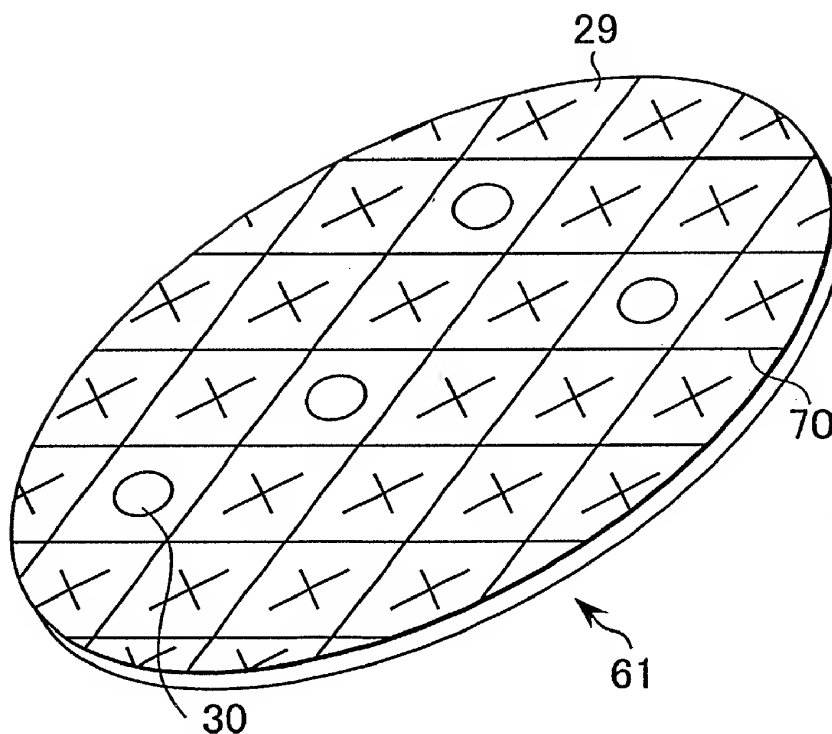
(Reg. #37,607)

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FIG.9



○:NON-DEFECTIVE CHIP

×:DEFECTIVE CHIP

Prior Art



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FIG.10A

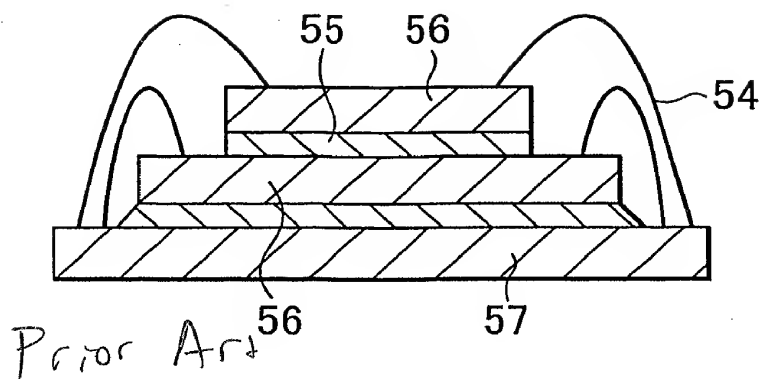


FIG.10B

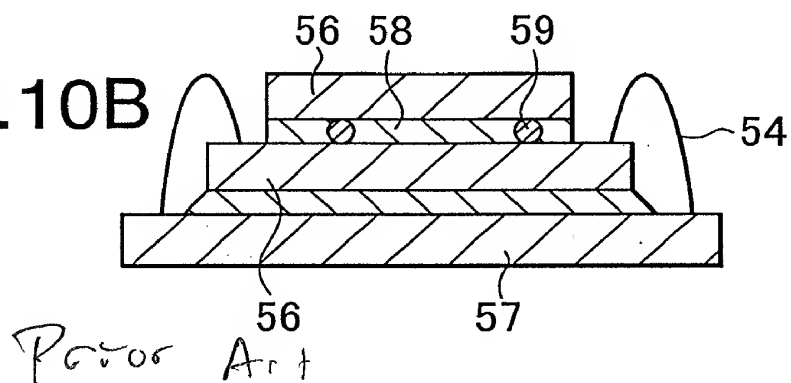
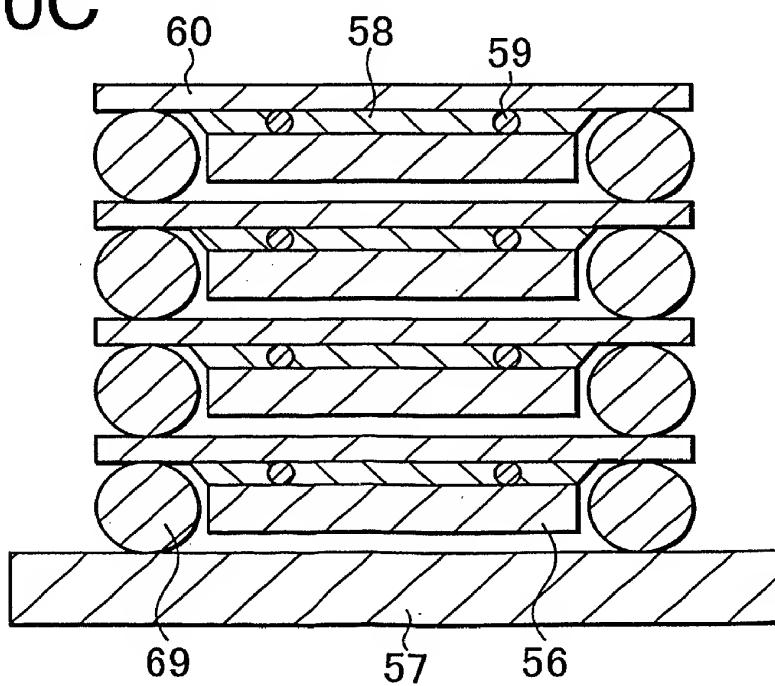


FIG.10C



Prior Art



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FIG.11A

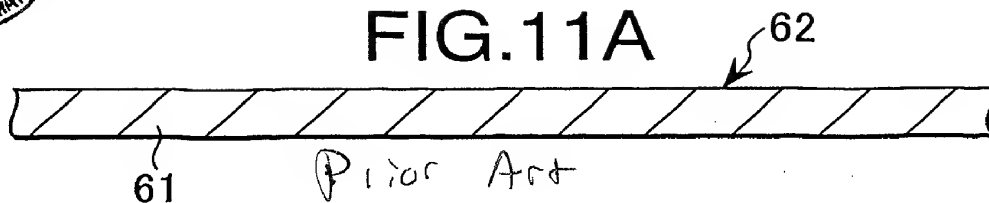


FIG.11B

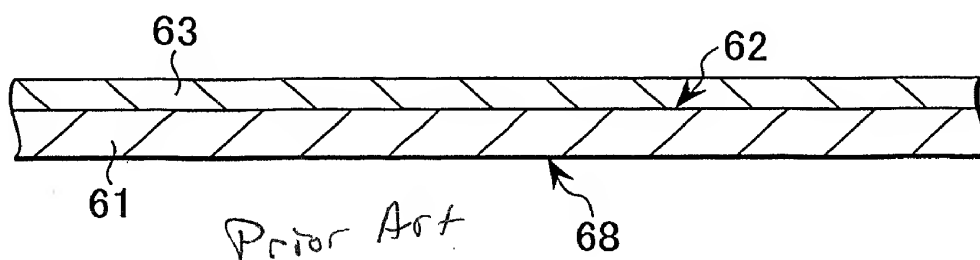


FIG.11C

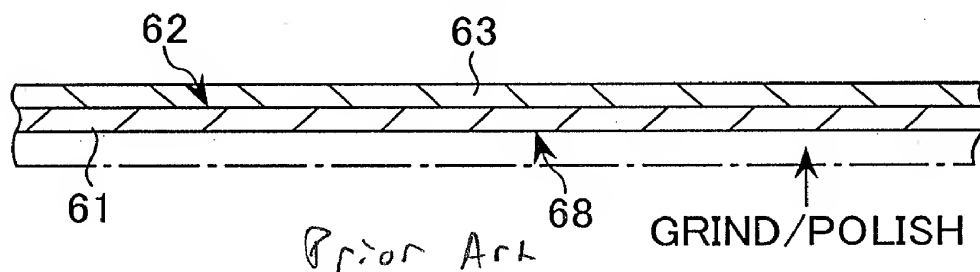


FIG.11D

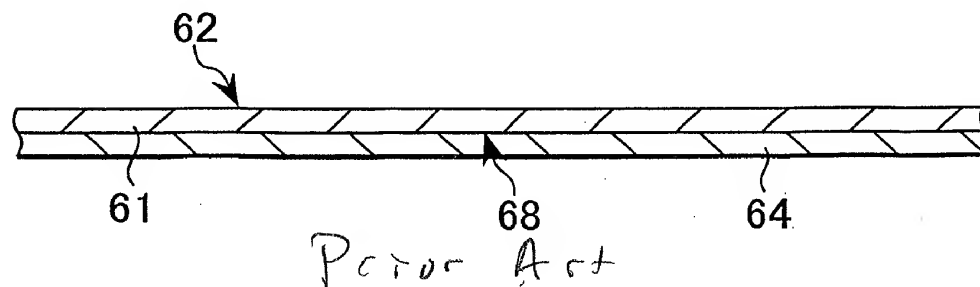
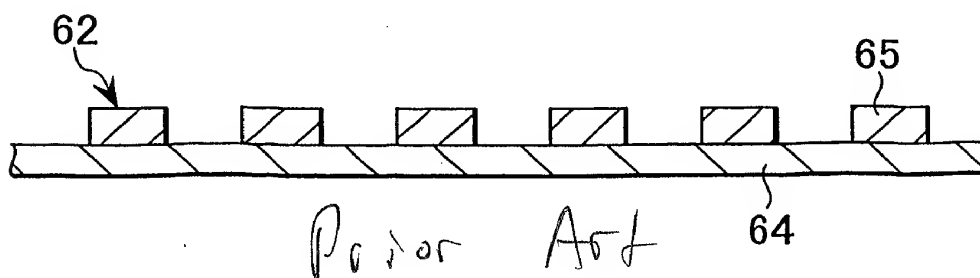
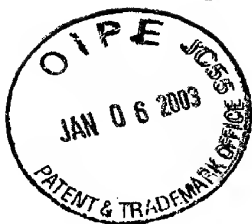


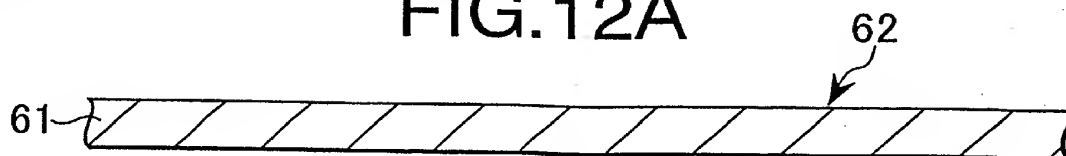
FIG.11E





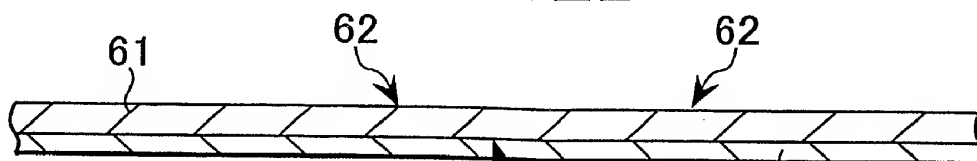
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FIG.12A



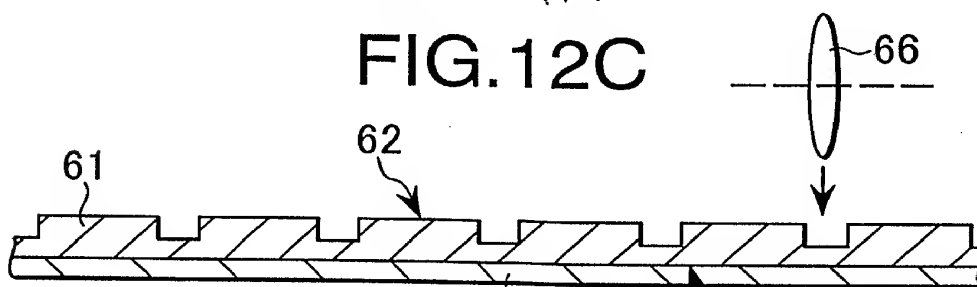
Prior Art

FIG.12B



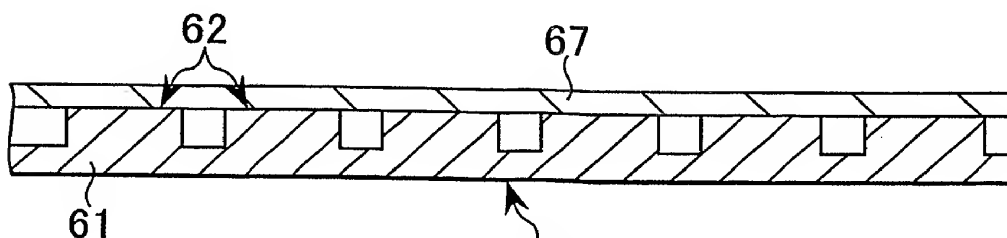
Prior Art

FIG.12C



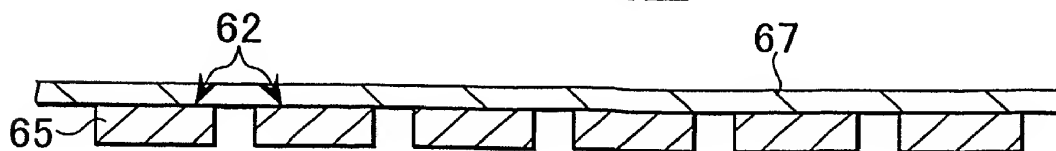
Prior Art

FIG.12D



Prior Art

FIG.12E



Prior Art